

FIG. 1

Comparator to generate NORM signal

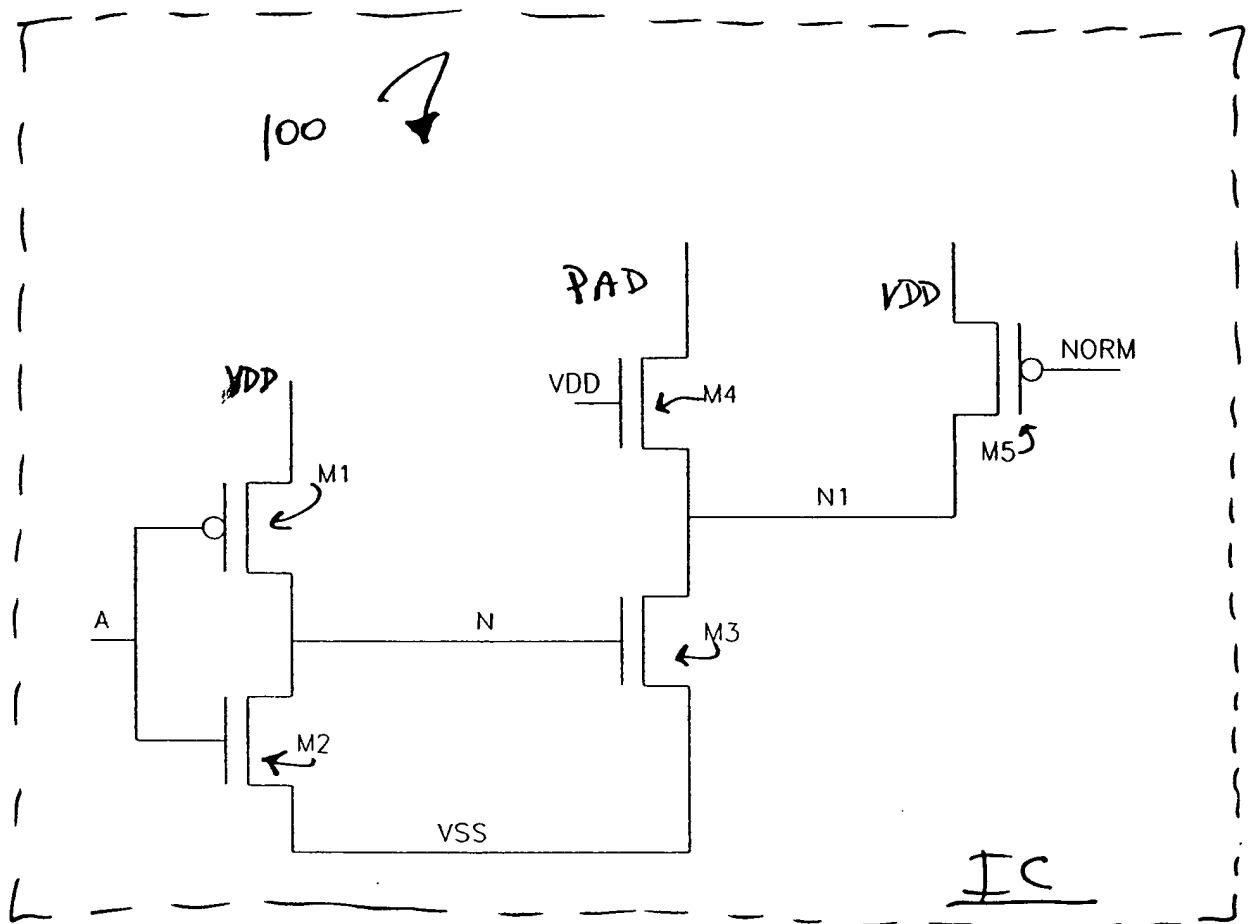


FIG. 2

Improved 5V Tolerant
Open Drain Output Buffer

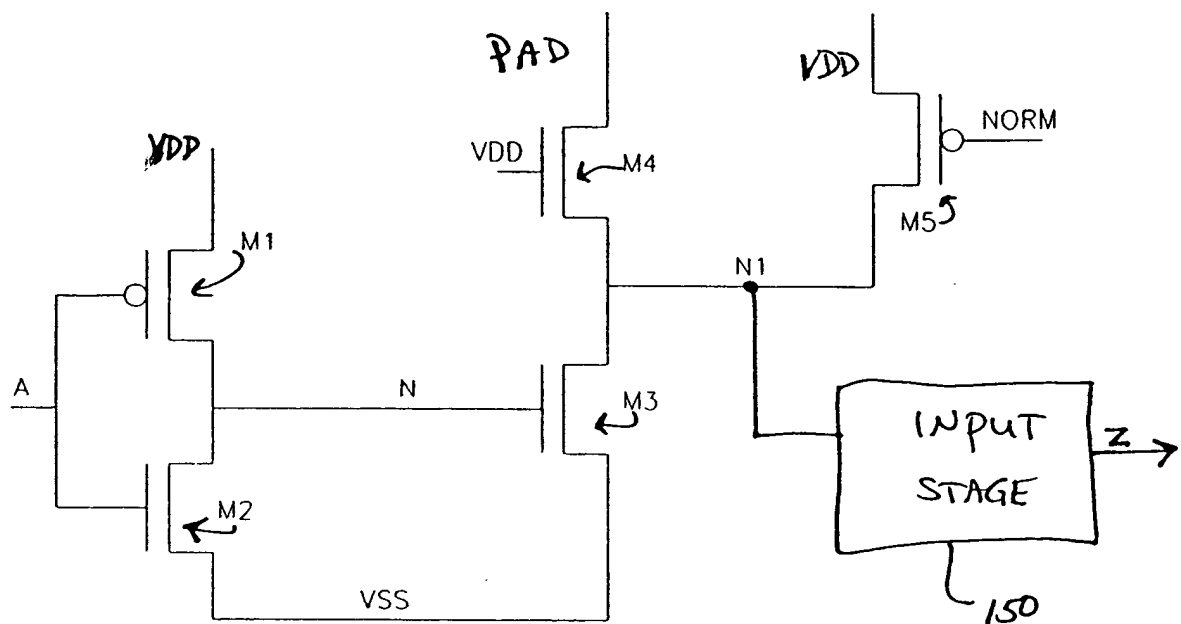


FIG. 3

Improved 5V Tolerant
Open Drain Bi-Directional Buffer

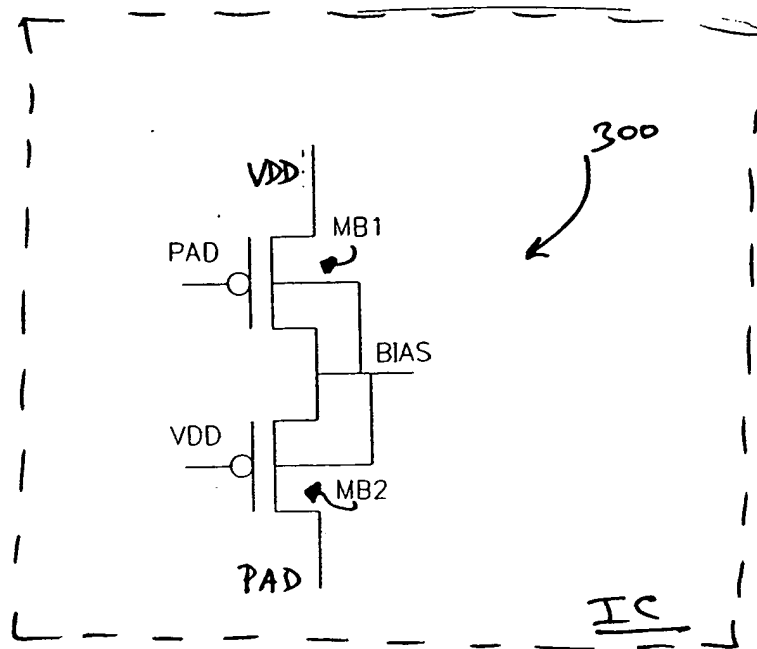


FIG. 4

Backgate Bias Generator

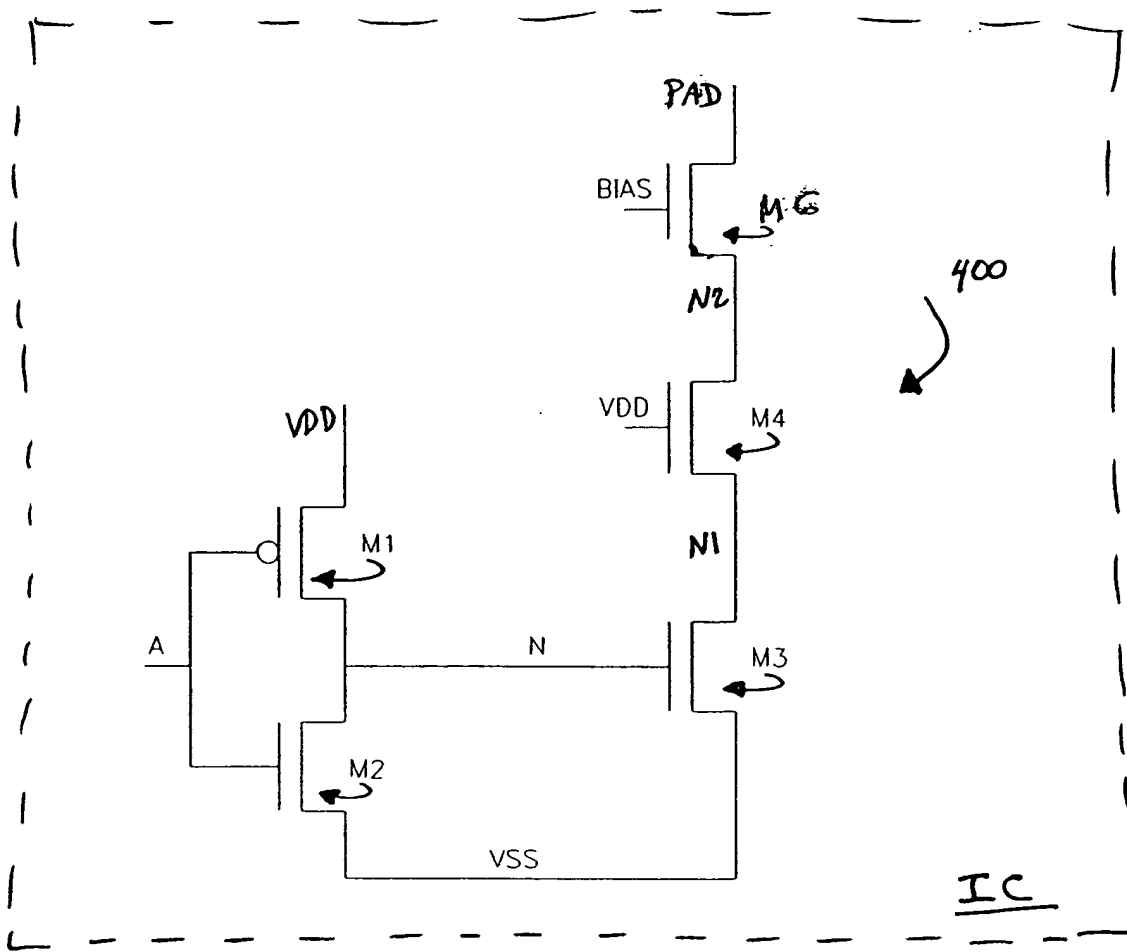


FIG. 5

Improved 5V Tolerant
Open Drain Output Buffer

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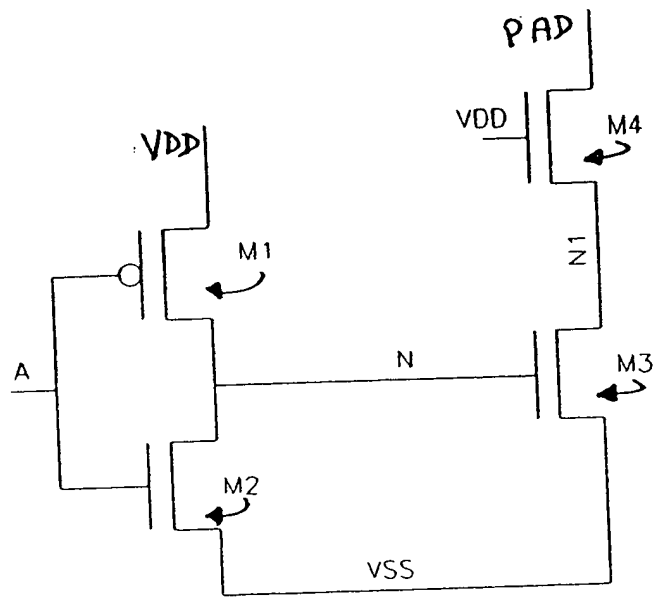


FIG. 7

5V Tolerant Open Drain Output Buffer
(Prior Art)